

Figure 1

FIG. 1 is a block diagram of a processor architecture.

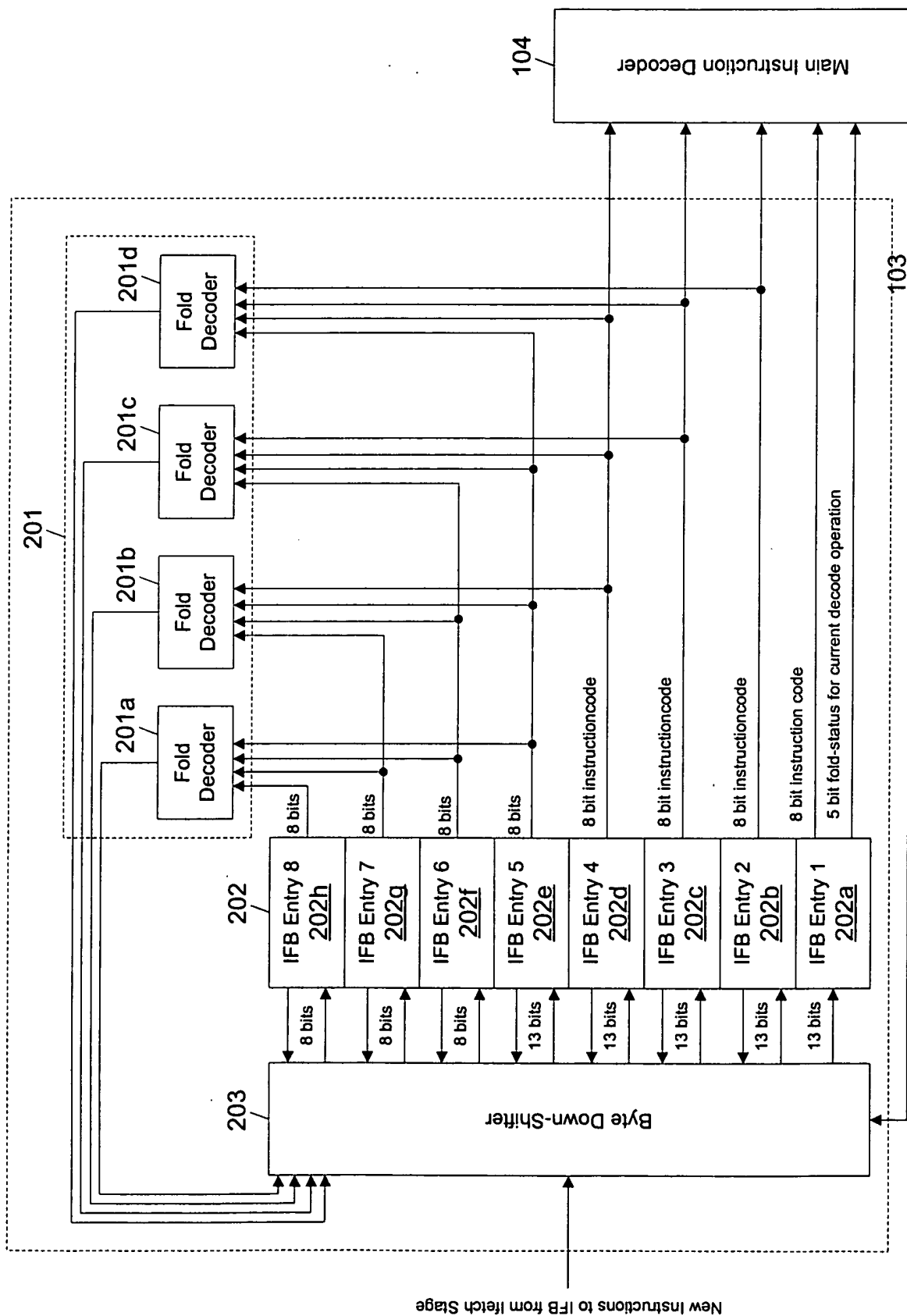


Figure 2